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2014

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2015 6 /

M

M

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DSP

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DSP

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	14:00 18:00

	11 16 5 18 6 26
	14:00 18:00
	Linux C

	11 16 5 18 6 26
	14:00 18:00

M

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5 11

F414

M

DSP

F406

F404

c

82314832

DSP

F414/F415

82314832

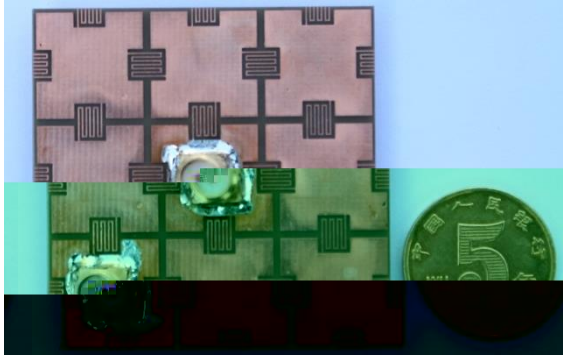
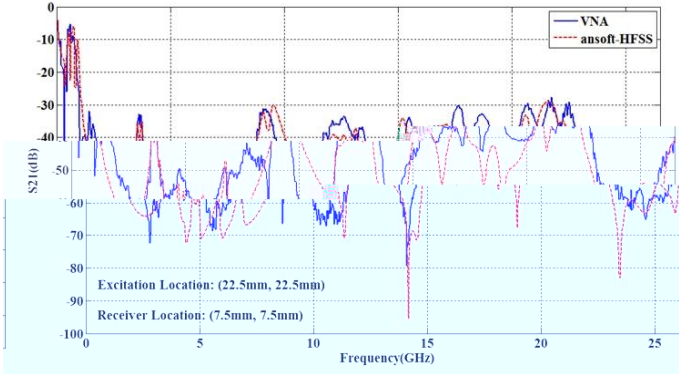
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MM M				6	36		
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			Linux	6		36	2
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<b>1</b>	EBG	EBG -30dB S21 0.8MHz-5GHz
<b>2</b>		
<b>3</b>		10GHz-12GHz >0dBm
<b>4</b>	FGPA	1 FPGA 2 12 AD AD7476 FPGA 3 FPGA 12 DA AD5621 4 FPGA DA AD
<b>5</b>	USB	1 USB 2 USB 3 USB 4 nRF2401 5 +USB + nRF2401
<b>6</b>		1 2 3 4 RS232 RS422 RS485
<b>7</b>		1 0~24V 0.002~3A 2
<b>8</b>		VHDL



	EBG		
	EBG		
	EBG	0.8MHz-5GHz	-30dB
S21	 <p style="text-align: center;">1 EBG</p>  <p style="text-align: center;">2 S21</p>		
	EBG		EBG
	EBG		

/	/
	1. 2. 3. ppt
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